

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Go IWASAKI

Serial No: Not assigned

Filed: November 25, 2003

For: OUTPUT BUFFER CIRCUIT AND SEMICONDUCTOR
MEMORY USING THE SAME

Art Unit: Not assigned

Examiner: Not assigned

CERTIFICATE OF MAILING VIA U.S. EXPRESS MAIL

"Express Mail" Mailing Label No. EV 325 214 875 US

Date of Deposit: November 25, 2003

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that

☒ two copies of a letter of transmittal

☒ patent application (43 page(s) of specification; 19 claim(s); 1 page(s) of abstract

☒ 11 sheet(s) of formal drawings

☒ certified copy of Japanese patent application No. 2002-344926 which was filed
November 28, 2002 from which priority is claimed in the subject case pursuant to 35
U.S.C. § 119

☒ Information Disclosure Statement with 2 references

☒ return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee"
service with sufficient postage under 37 C.F.R. § 1.10 on the date indicated above and are addressed to:

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Arlington, VA 22313-1450

Date: November 25, 2003

Hogan & Hartson, LLP
500 South Grand Avenue, Suite 1900
Los Angeles, California 90071
Telephone: 213-337-6700
Facsimile: 213-337-6701

Reynaldo Gallardo

Name of person mailing papers

Signature